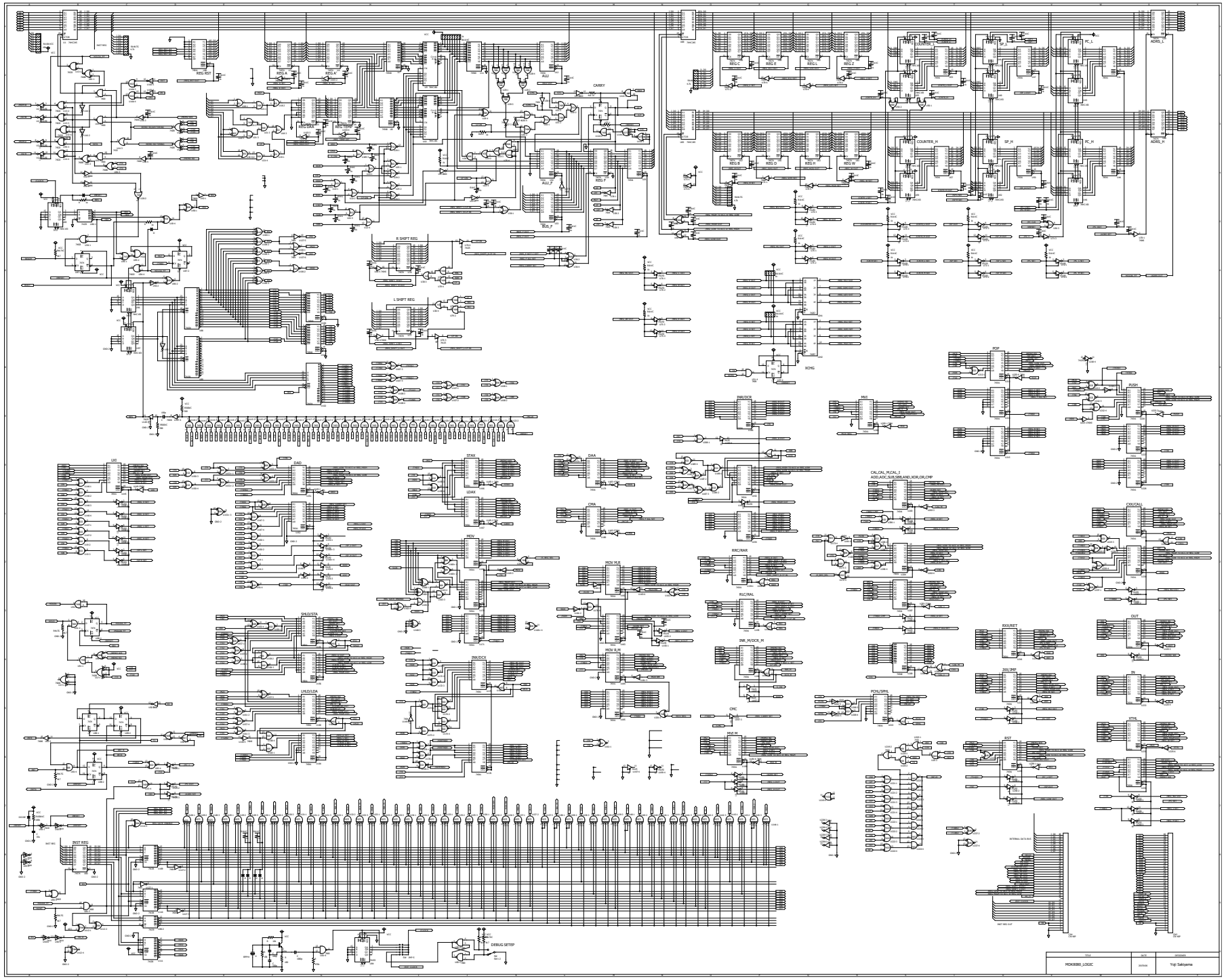
REG A
REG B
REG C
REG D
REG E
REG H
REG L
REG W
REG Z
ADD STA
ADD TEMP
ALU
CARRY
ALU_F
REG F
BUS_F
COUNTER
COUNTER_H
SP_L
SP_H
PC_L
PC_H
ADRS_L
ADRS_H
R SHIFT REG
L SHIFT REG
XCHG
POP
PUSH
INR/DCR
MVI
LXI
DAD
STAX
LDAX
DAA
CMA
MOV
MOV MLR
MOV RLM
INR/DCR
INR_M/DCR_M
CMC
MOV M
RRC/RAR
RLC/RAL
CAL/CAL_M/CAL_I
ADD,ADC,SUB,SBB,AND,XOR,OR,CMP
CNC/CALL
RNZ/RET
JNZ/JMP
PCHL/SPHL
OUT
IN
XTHL
RST
SHLD/STA
LHLD/LDA
INST REG
DEBUG SETEP
TITLE
MDK8080_LOGIC
DATE
2020/08
DESIGNER
Yoji Sakiyama